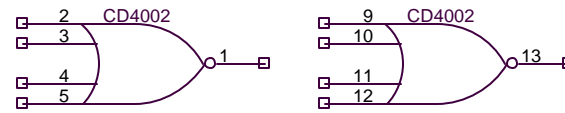


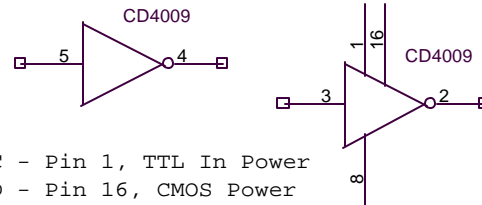
VDD - 14 (+3 to +15 V)  
VSS - 7 (Ground)

Quad 2- Input NOR Gate

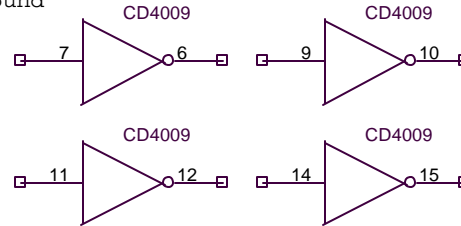


VDD - 14 (+3 to +15 V)  
VSS - 7 (Ground)

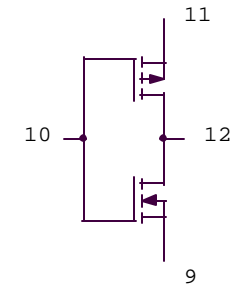
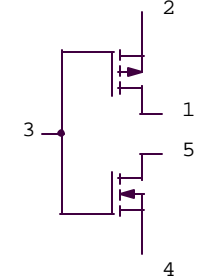
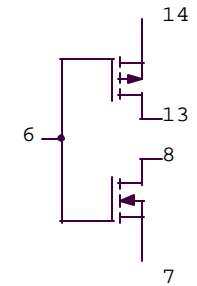
Dual 4-Input NOR Gate



VCC - Pin 1, TTL In Power  
VDD - Pin 16, CMOS Power  
VSS - Pin 8, Common  
Ground



CD4009 TTL to CMOS Inverting Buffer



VDD - 14 (+3 to +15 V)  
VSS - 7 (Ground)

CD4007 Dual Complementary  
Pair Plus Inverter

Title		
CMOS Class		
Size	Document Number	Rev
A	Design 72	1
Date: Tuesday, March 20, 2007		Sheet 1 of 2